

WHAT IS CLAIMED IS:

1. A microcomputer comprising a USB interface circuit for  
interfacing transmission and receipt of data between a host and  
5 the microcomputer through a USB signal line, comprising state  
setting means for setting the USB signal line to a level in a  
non-connection state for a period before the USB interface circuit  
can respond to a bus reset signal sent from the host.

10 2. A microcomputer according to claim 1, wherein the state  
setting means includes an output terminal to be also used as  
a terminal to which the USB signal line is connected, a reset  
signal generating circuit for generating a reset signal upon  
receipt of power supply from the USB signal line, and a first  
15 oscillator for starting oscillation immediately in response to  
the reset signal, and operates the microcomputer by using an  
output of the first oscillator as a system clock and sets a level  
of the output terminal, thereby forcibly setting the USB signal  
line to a level in a non-connection state.

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3. A microcomputer according to claim 2, further comprising  
a second oscillator serving to start oscillation in response  
to the reset signal and having a stabler oscillation frequency  
and a longer oscillation starting time than those of the first  
25 oscillator, and a selecting circuit for selecting one output  
from the first and second oscillators and feeding the selected

output for a system clock, wherein the selecting circuit selects the output of the first oscillator in response to the reset signal and selects the output of the second oscillator after an oscillation state of the second oscillator is stabilized.

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4. A microcomputer according to claim 3, further comprising an adding circuit for inputting the reset signal and a delay reset signal obtained by delaying the reset signal, wherein the selecting circuit selects one output from the first oscillator and the output of the second oscillator in response to an output of the adding circuit.

5. A microcomputer according to claim 2, wherein the first oscillator is an RC oscillator.

6. A microcomputer according to claim 3, wherein the first oscillator is an RC oscillator.

7. A microcomputer according to claim 4, wherein the first oscillator is an RC oscillator.

8. A microcomputer according to claim 3, wherein the second oscillator is a quartz oscillator.

9. A microcomputer according to claim 4, wherein the second oscillator is a quartz oscillator.

[illegible]

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